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APPLICANTS

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** CONTINUING DATA *****

This application is a DIV of 09/247,782 02/09/1999 PAT 6,252,282

yes all
02/10/2005

** FOREIGN APPLICATIONS *****

EUROPEAN PATENT OFFICE (EPO) 98200394.9 02/09/1998

yes all
02/10/2005

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 05/31/2001

Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> Met after met	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	NETHERLANDS	2	5	1

Allowance
 Examiner's Signature
 Initials

ADDRESS

24738
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 SAN JOSE, CA
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TITLE

Semiconductor device with a bipolar transistor, and method of manufacturing such a device

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time)
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